

IN THE CLAIMS

The following listing of claims will replace all prior versions and listings of claims in the application.

Listing of Claims

1. (previously presented) A method of reprogramming the memory of an electronic module comprising the steps of:

down-loading a boot loader program and initializing software to a first portion of the module memory via a wired bus at a first programming station to enable the module to receive information via preselected wireless protocol; and

thereafter down-loading wirelessly new application and program software to a second portion of the module memory via the preselected wireless protocol at a second programming station.
2. (currently amended) The method of claim [[2]] 1 wherein the preselected wireless protocol comprises an open systems standard.
3. (original) The method of claim 1 wherein the first portion of the module memory is smaller than the second portion.
4. (original) The method of claim 3 wherein speed of information transmission over the wired bus is slower than speed of information transmission via the wireless protocol.

5. (previously presented) For use in an assembly line of units, each incorporating an electronic programmable module having flash memory, a method of flashing said memory comprising the steps of:

positioning each unit at a first flashing station and down-loading a boot loader program and operating system kernel via a wired bus to the module to enable the module to receive information via a preselected wireless protocol; and

positioning each unit at a second flashing station downstream on the assembly line from the first station and wirelessly down-loading application and program software to the module via the preselected wireless protocol.

6. (original) The method of claim 5 wherein the wired bus is incorporated into each unit.

7. (previously presented) The method of claim 5 wherein the preselected wireless protocol comprises an open systems standard.

8. (withdrawn) For use in an assembly line of vehicles, each incorporating a wired vehicle bus, an electronic programmable control module having a processor, flash memory, a wired vehicle bus interface and circuitry for wireless reception of data, a method of flashing the flash memory to meet specifications of a particular vehicle being assembled, the method comprising:

installing a generic control module in the particular vehicle;

down-loading, at a first programming station of the assembly line, via the

wired vehicle bus and control module vehicle bus interface a boot loader program and initializing software to a first portion of the flash memory to enable the control module to subsequently receive information via a preselected wireless protocol; and

wirelessly down-loading, at a second programming station of the assembly line downstream of the first programming station, application and program software to a second portion of the flash memory to meet the specifications of the particular vehicle.

9. (withdrawn) The method of claim 8 wherein the preselected wireless protocol comprises an open systems standard.

10. (withdrawn) The method of claim 9 wherein the open systems standard comprises Bluetooth.

11. (withdrawn) The method of claim 8 wherein the first portion of the flash memory is smaller than the second portion.

12. (withdrawn) The method of claim 11 wherein speed of down-loading over the wired vehicle bus is slower than speed of down-loading via the preselected wireless protocol.